

#### TOSHIBA Diode Silicon Epitaxial Planar Type

# **1SS309**

### **Ultra High Speed Switching Applications**

• Small package : SC-74A

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	VRM	85	V	
Reverse voltage	VR	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	300 (*)	mA	
Average forward current	lo	100 (*)	mA	
Surge current (10ms)	I <sub>FSM</sub>	2 (*)	Α	
Power dissipation	P <sub>D</sub> (Note 1, 3)	300	mW	
	P <sub>D</sub> (Note 2)	200		
Junction temperature	T <sub>j</sub> (Note 1)	150	°C	
	T <sub>j</sub> (Note 2)	125		
Storage temperature	T <sub>stg</sub> (Note 1)	−55 to 150	°C	
	T <sub>stg</sub> (Note 2)	-55 to 125		

Unit: mm

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Weight: 0.014g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.)

may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: For devices with the ordering part number ending in LF(T.

Note 2: For devices with the ordering part number in other than LF(T.

Note 3: Total rating.

(\*): Unit rating. Total rating = unit rating x 1.5

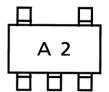
#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I <sub>F</sub> = 1 mA	_	0.60	_	V
	VF (2)	I <sub>F</sub> = 10 mA	_	0.72	_	
	V <sub>F</sub> (3)	I <sub>F</sub> = 100 mA	_	0.90	1.20	
Reverse current	I <sub>R (1)</sub>	V <sub>R</sub> = 30 V	_	_	0.1	μΑ
	IR (2)	V <sub>R</sub> = 80 V	_	_	0.5	
Total capacitance	Ст	V <sub>R</sub> = 0 V, f = 1 MHz	_	0.9	3.0	pF
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 10 mA, Fig.1	_	1.6	4.0	ns

Start of commercial production 1987-07



## Marking



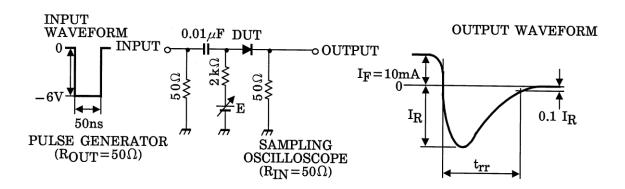
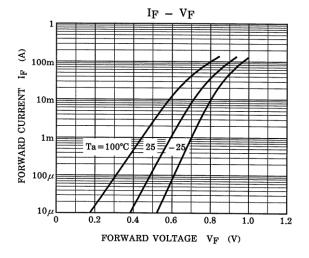
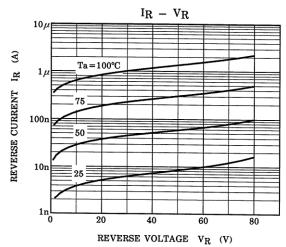


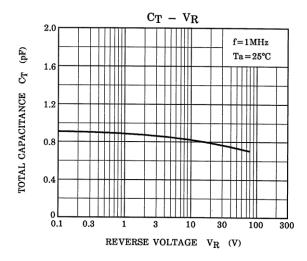
Fig.1 Reverse recovery time (t<sub>rr</sub>) test circuit

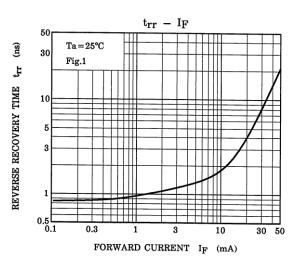


#### **Characteristics Curves**









The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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