

**Automotive Grade, Fully Integrated, Hall-Effect-Based Linear Current Sensor IC  
with 2.1 kVRMS Voltage Isolation and Low-Resistance Current Conductor**

## Not for New Design

The ACS714 is in production but has been determined to be NOT FOR NEW DESIGN. This classification indicates that sale of this device is currently restricted to existing customer applications. The device should not be purchased for new design applications because obsolescence in the near future is probable. Samples are no longer available.

Date of status change: March 14, 2025

### Recommended Substitutions:

*For existing customer transition, and for new customers or new applications, refer to [ACS71240](#), [ACS37010](#), or [ACS724](#).*

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NOTE: For detailed information on purchasing options, contact your local Allegro field applications engineer or sales representative.

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## Automotive Grade, Fully Integrated, Hall-Effect-Based Linear Current Sensor IC with 2.1 kVRMS Voltage Isolation and Low-Resistance Current Conductor

### FEATURES AND BENEFITS

- Low-noise analog signal path
- Device bandwidth is set via the FILTER pin
- 5  $\mu$ s output rise time in response to step input current
- 80 kHz bandwidth
- Total output error 1.5% typical, at  $T_A = 25^\circ\text{C}$
- Small footprint, low-profile SOIC8 package
- 1.2 m $\Omega$  internal conductor resistance
- 2.1 kVRMS minimum isolation voltage from pins 1-4 to pins 5-8
- 5.0 V, single supply operation
- 66 to 185 mV/A output sensitivity
- Output voltage proportional to AC or DC currents
- Factory-trimmed for accuracy
- Extremely stable output offset voltage
- Nearly zero magnetic hysteresis
- Ratiometric output from supply voltage
- Operating temperature range,  $-40^\circ\text{C}$  to  $150^\circ\text{C}$



TÜV America  
Certificate Number:  
U8V 15 05 54214 038  
CB 13 06 54214 026

### Package: 8-pin SOIC (suffix LC)



*Not to scale*

### DESCRIPTION

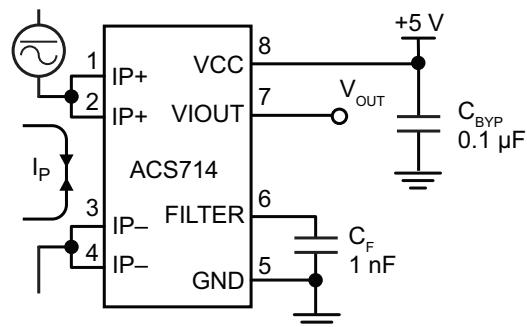
The Allegro™ ACS714 provides economical and precise solutions for AC or DC current sensing in automotive systems. The device package allows for easy implementation by the customer. Typical applications include motor control, load detection and management, switch-mode power supplies, and overcurrent fault protection.

The device consists of a precise, low-offset, linear Hall circuit with a copper conduction path located near the surface of the die. Applied current flowing through this copper conduction path generates a magnetic field which the Hall IC converts into a proportional voltage. Device accuracy is optimized through the close proximity of the magnetic signal to the Hall transducer. A precise, proportional voltage is provided by the low-offset, chopper-stabilized BiCMOS Hall IC, which is programmed for accuracy after packaging.

The output of the device has a positive slope ( $>V_{IOUT(Q)}$ ) when an increasing current flows through the primary copper conduction path (from pins 1 and 2, to pins 3 and 4), which is the path used for current sampling. The internal resistance of this conductive path is 1.2 m $\Omega$  typical, providing low power loss. The thickness of the copper conductor allows survival of the device at up to 5 $\times$  overcurrent conditions. The terminals of the conductive path are electrically isolated from the signal leads (pins 5 through 8). This allows the ACS714 to be used

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### Typical Application



Application 1. The ACS714 outputs an analog signal,  $V_{OUT}$ , that varies linearly with the uni- or bi-directional AC or DC primary sampled current,  $I_P$ , within the range specified.  $C_F$  is recommended for noise management, with values that depend on the application.

**DESCRIPTION (continued)**

in applications requiring electrical isolation without the use of opto-isolators or other costly isolation techniques.

The ACS714 is provided in a small, surface mount SOIC8 package. The leadframe is plated with 100% matte tin, which is compatible

with standard lead (Pb) free printed circuit board assembly processes. Internally, the device is Pb-free, except for flip-chip high-temperature Pb-based solder balls, currently exempt from RoHS. The device is fully calibrated prior to shipment from the factory.

**SELECTION GUIDE**

Part Number	Current Sensing Range, $I_p$ (A)	Sensitivity, Sens (Typ) (mV/A)	$T_A$ (°C)	Packing*
ACS714ELCTR-05B-T	±5	185	–40 to 85 –40 to 150	Tape and reel, 3000 pieces/reel
ACS714ELCTR-20A-T	±20	100		
ACS714ELCTR-30A-T	±30	66		
ACS714LLCTR-05B-T	±5	185		
ACS714LLCTR-20A-T	±20	100		
ACS714LLCTR-30A-T	±30	66		
ACS714LLCTR-50A-T	±50	40		

\*Contact Allegro for additional packing options.

**ABSOLUTE MAXIMUM RATINGS**

Characteristic	Symbol	Notes	Rating	Units
Supply Voltage	$V_{CC}$		8	V
Reverse Supply Voltage	$V_{RCC}$		–0.1	V
Output Voltage	$V_{IOUT}$		8	V
Reverse Output Voltage	$V_{RIOUT}$		–0.1	V
Output Current Source	$I_{IOUT(Source)}$		3	mA
Output Current Sink	$I_{IOUT(Sink)}$		10	mA
Overcurrent Transient Tolerance	$I_p$	1 pulse, 100 ms	100	A
Nominal Operating Ambient Temperature	$T_A$	Range E	–40 to 85	°C
		Range L	–40 to 150	°C
Maximum Junction Temperature	$T_J(max)$		165	°C
Storage Temperature	$T_{stg}$		–65 to 170	°C

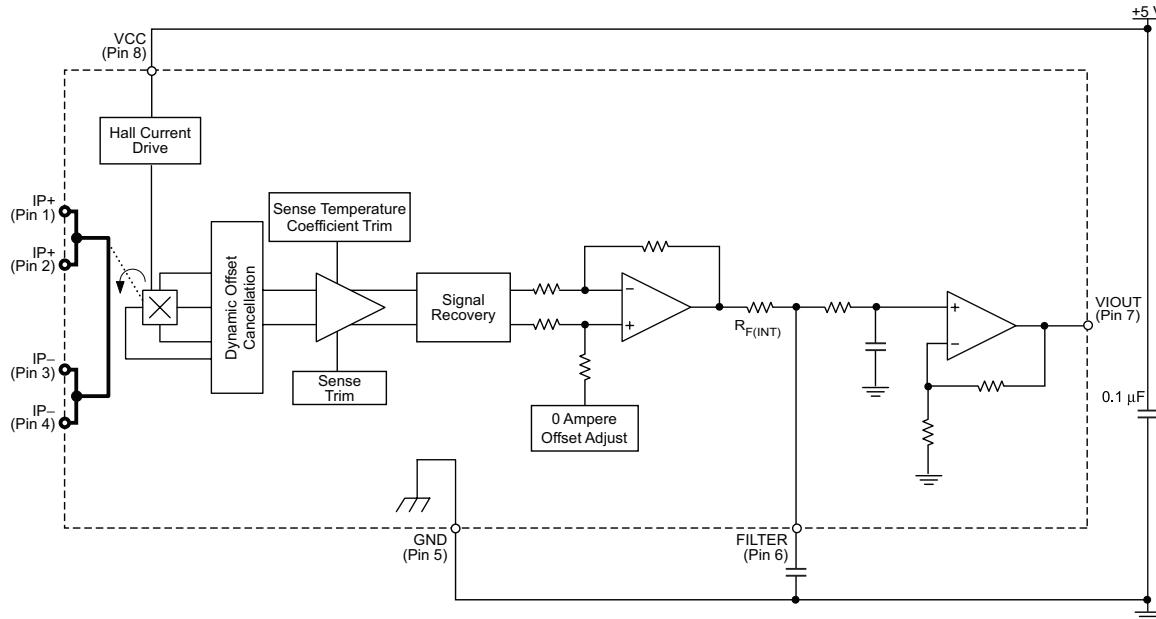
**ISOLATION CHARACTERISTICS**

Characteristic	Symbol	Notes	Rating	Unit
Dielectric Strength Test Voltage*	$V_{ISO}$	Agency type-tested for 60 seconds per UL standard 60950-1, 1st Edition	2100	VAC
Working Voltage for Basic Isolation	$V_{WFSI}$	For basic (single) isolation per UL standard 60950-1, 1st Edition	354	VDC or $V_{pk}$
Working Voltage for Reinforced Isolation	$V_{WFRI}$	For reinforced (double) isolation per UL standard 60950-1, 1st Edition	184	VDC or $V_{pk}$

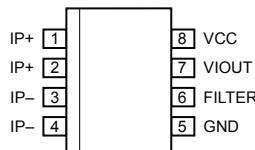
\* Allegro does not conduct 60-second testing. It is done only during the UL certification process.

Parameter	Specification
Fire and Electric Shock	CAN/CSA-C22.2 No. 60950-1-03 UL 60950-1:2003 EN 60950-1:2001

### Functional Block Diagram



### Pinout Diagram



### Terminal List

Number	Name	Description
1 and 2	IP+	Terminals for current being sampled; fused internally
3 and 4	IP-	Terminals for current being sampled; fused internally
5	GND	Signal ground terminal
6	FILTER	Terminal for external capacitor that sets bandwidth
7	VOUT	Analog output signal
8	VCC	Device power supply terminal

COMMON OPERATING CHARACTERISTICS [1]: Over full range of  $T_A$ ,  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
<b>ELECTRICAL CHARACTERISTICS</b>						
Supply Voltage	$V_{CC}$		4.5	5.0	5.5	V
Supply Current	$I_{CC}$	$V_{CC} = 5.0 \text{ V}$ , output open	–	10	13	mA
Output Capacitance Load	$C_{LOAD}$	VOUT to GND	–	–	10	nF
Output Resistive Load	$R_{LOAD}$	VOUT to GND	4.7	–	–	kΩ
Primary Conductor Resistance	$R_{PRIMARY}$	$T_A = 25^\circ\text{C}$	–	1.2	–	mΩ
Rise Time	$t_r$	$I_P = I_P(\text{max})$ , $T_A = 25^\circ\text{C}$ , $C_{OUT} = \text{open}$	–	3.5	–	μs
Frequency Bandwidth	$f$	–3 dB, $T_A = 25^\circ\text{C}$ ; $I_P$ is 10 A peak-to-peak	–	80	–	kHz
Nonlinearity	$E_{LIN}$	Over full range of $I_P$	–	1.5	–	%
Symmetry	$E_{SYM}$	Over full range of $I_P$	98	100	102	%
Zero Current Output Voltage	$V_{IOUT(Q)}$	Bidirectional; $I_P = 0 \text{ A}$ , $T_A = 25^\circ\text{C}$	–	$V_{CC} \times 0.5$	–	V
Power-On Time	$t_{PO}$	Output reaches 90% of steady-state level, $T_J = 25^\circ\text{C}$ , 20 A present on leadframe	–	35	–	μs
Magnetic Coupling [2]			–	12	–	G/A
Internal Filter Resistance [3]	$R_{F(INT)}$			1.7		kΩ

[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient,  $T_A$ , and internal leadframe temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

[2] 1G = 0.1 mT.

[3]  $R_{F(INT)}$  forms an RC circuit via the FILTER pin.

## COMMON THERMAL CHARACTERISTICS [1]

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Operating Internal Leadframe Temperature	$T_A$	E range	–40	–	85	°C
		L range	–40	–	150	°C

[1] Additional thermal information is available on the Allegro website.

x05B PERFORMANCE CHARACTERISTICS [1]:  $T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$  (range E),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-5	-	5	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	180	185	190	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 185 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	21	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.26	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.08	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.054	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.008	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-40	-	40	mV
Total Output Error [2]	$E_{TOT}$	$I_P = \pm 5 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	$\pm 1.5$	-	%

[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_{J(\text{max})}$ , is not exceeded.

[2] Percentage of  $I_P$ , with  $I_P = 5 \text{ A}$ . Output filtered.

x05B PERFORMANCE CHARACTERISTICS [1]:  $T_A = -40^\circ\text{C}$  to  $150^\circ\text{C}$  (range L),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-5	-	5	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	-	185	-	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 185 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	21	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.26	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.08	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.054	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.008	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-60	-	60	mV
Total Output Error [2]	$E_{TOT}$	$I_P = \pm 5 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	$\pm 1.5$	-	%
		$I_P = \pm 5 \text{ A}$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	-7	-	7	%

[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_{J(\text{max})}$ , is not exceeded.

[2] Percentage of  $I_P$ , with  $I_P = 5 \text{ A}$ . Output filtered.

x20A PERFORMANCE CHARACTERISTICS<sup>[1]</sup>:  $T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$  (Range E),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-20	-	20	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	96	100	104	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 100 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	11	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.34	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.07	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.017	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.004	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-30	-	30	mV
Total Output Error <sup>[2]</sup>	$E_{TOT}$	$I_P = \pm 20 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	±1.5	-	%

<sup>[1]</sup> Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

<sup>[2]</sup> Percentage of  $I_P$ , with  $I_P = 20 \text{ A}$ . Output filtered.

x20A PERFORMANCE CHARACTERISTICS<sup>[1]</sup>:  $T_A = -40^\circ\text{C}$  to  $150^\circ\text{C}$  (range L),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-20	-	20	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	-	100	-	mV/A
		Over full range of $I_P$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	94	-	104	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 100 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{out}$ , 2 kHz bandwidth	-	11	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.34	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.07	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.017	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.004	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-40	-	40	mV
Total Output Error <sup>[2]</sup>	$E_{TOT}$	$I_P = \pm 20 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	±1.5	-	%
		$I_P = \pm 20 \text{ A}$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	-5	-	5	%

<sup>[1]</sup> Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

<sup>[2]</sup> Percentage of  $I_P$ , with  $I_P = 20 \text{ A}$ . Output filtered.

x30A PERFORMANCE CHARACTERISTICS<sup>[1]</sup>:  $T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$  (range E),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-30	-	30	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	64	66	68	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 66 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	7	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.35	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.08	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.007	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.002	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-30	-	30	mV
Total Output Error <sup>[2]</sup>	$E_{TOT}$	$I_P = \pm 30 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	±1.5	-	%

[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

[2] Percentage of  $I_P$ , with  $I_P = 30 \text{ A}$ . Output filtered.

x30A PERFORMANCE CHARACTERISTICS<sup>[1]</sup>:  $T_A = -40^\circ\text{C}$  to  $150^\circ\text{C}$  (range L),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-30	-	30	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	-	66	-	mV/A
		Over full range of $I_P$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	63	-	69	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 66 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	7	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.35	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.08	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.007	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.002	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$	-40	-	40	mV
Total Output Error <sup>[2]</sup>	$E_{TOT}$	$I_P = \pm 30 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	±1.5	-	%
		$I_P = \pm 30 \text{ A}$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	-5	-	5	%

[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

[2] Percentage of  $I_P$ , with  $I_P = 30 \text{ A}$ . Output filtered.

x50A PERFORMANCE CHARACTERISTICS<sup>[1]</sup>:  $T_A = -40^\circ\text{C}$  to  $150^\circ\text{C}$  (range L),  $C_F = 1 \text{ nF}$ , and  $V_{CC} = 5 \text{ V}$ , unless otherwise specified

Characteristic	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Optimized Accuracy Range	$I_P$		-50	-	50	A
Sensitivity	Sens	Over full range of $I_P$ , $T_A = 25^\circ\text{C}$	-	40	-	mV/A
		Over full range of $I_P$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	37.5	-	42.5	mV/A
Noise	$V_{NOISE(PP)}$	Peak-to-peak, $T_A = 25^\circ\text{C}$ , 40 mV/A programmed Sensitivity, $C_F = 47 \text{ nF}$ , $C_{OUT} = \text{open}$ , 2 kHz bandwidth	-	4.5	-	mV
Zero Current Output Slope	$\Delta V_{OUT(Q)}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	-0.35	-	mV/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.08	-	mV/°C
Sensitivity Slope	$\Delta \text{Sens}$	$T_A = -40^\circ\text{C}$ to $25^\circ\text{C}$	-	0.007	-	mV/A/°C
		$T_A = 25^\circ\text{C}$ to $150^\circ\text{C}$	-	-0.002	-	mV/A/°C
Electrical Output Voltage	$V_{OE}$	$I_P = 0 \text{ A}$ , $T_A = 25^\circ\text{C}$	-40	-	40	mV
Total Output Error <sup>[2]</sup>	$E_{TOT}$	$I_P = \pm 40 \text{ A}$ , $T_A = 25^\circ\text{C}$	-	±1.5	-	%
		$I_P = \pm 40 \text{ A}$ , $T_A = -40^\circ\text{C}$ to $150^\circ\text{C}$	-	±6	-	%

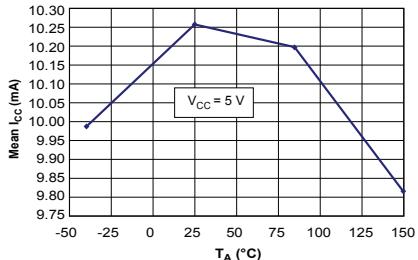
[1] Device may be operated at higher primary current levels,  $I_P$ , and ambient temperatures,  $T_A$ , provided that the Maximum Junction Temperature,  $T_J(\text{max})$ , is not exceeded.

[2] Percentage of  $I_P$ , with  $I_P = 40 \text{ A}$ . Output filtered.

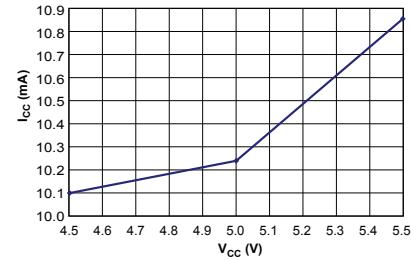
## CHARACTERISTIC PERFORMANCE

 $I_p = 5$  A, unless otherwise specified

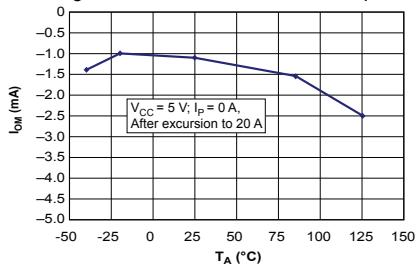
Mean Supply Current versus Ambient Temperature



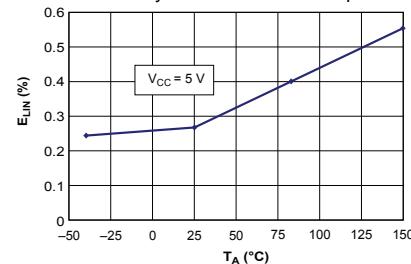
Supply Current versus Supply Voltage



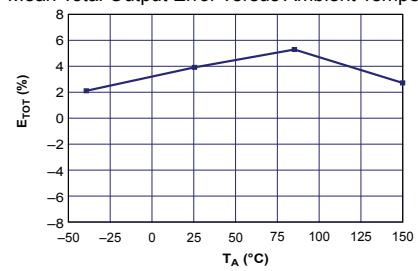
Magnetic Offset versus Ambient Temperature



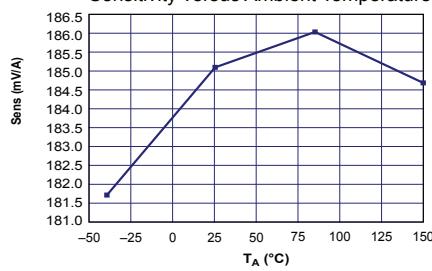
Nonlinearity versus Ambient Temperature



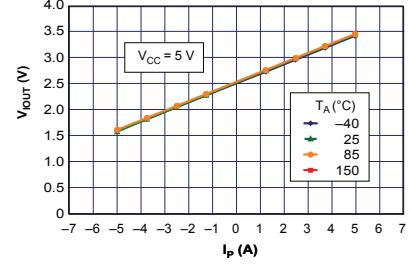
Mean Total Output Error versus Ambient Temperature



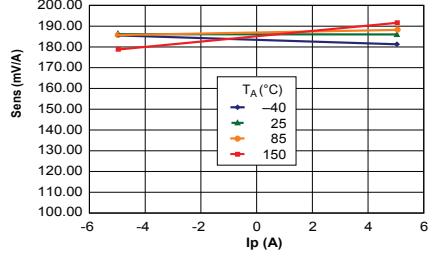
Sensitivity versus Ambient Temperature



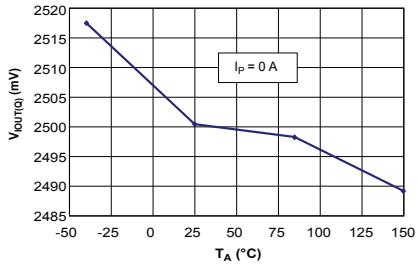
Output Voltage versus Sensed Current



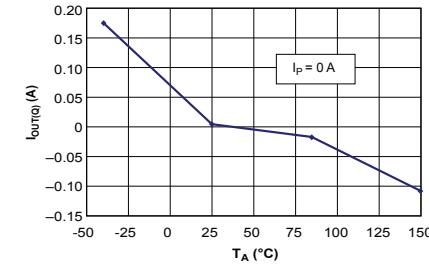
Sensitivity versus Sensed Current



0 A Output Voltage versus Ambient Temperature



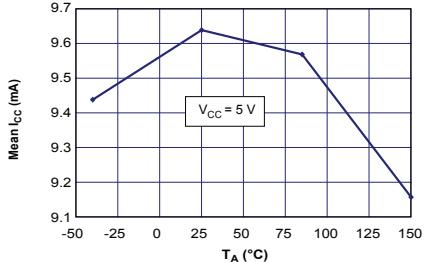
0 A Output Voltage Current versus Ambient Temperature



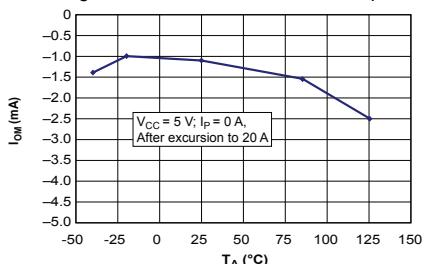
## CHARACTERISTIC PERFORMANCE

 $I_p = 20$  A, unless otherwise specified

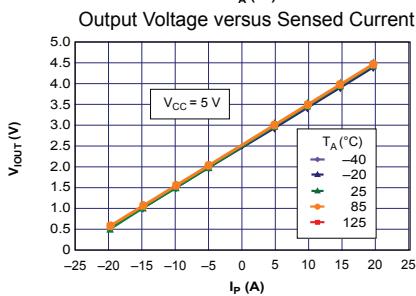
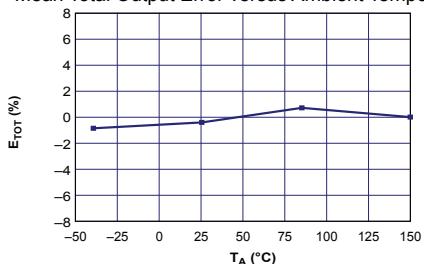
Mean Supply Current versus Ambient Temperature



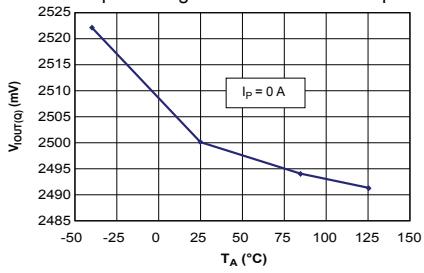
Magnetic Offset versus Ambient Temperature



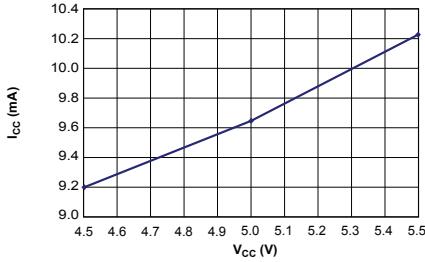
Mean Total Output Error versus Ambient Temperature



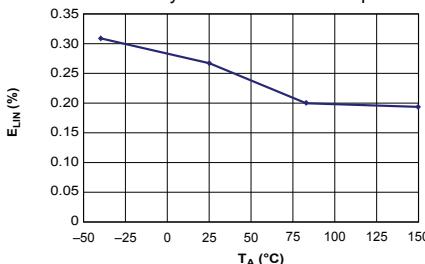
0 A Output Voltage versus Ambient Temperature



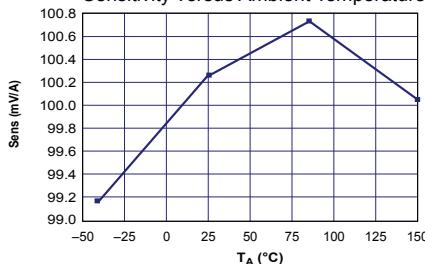
Supply Current versus Supply Voltage



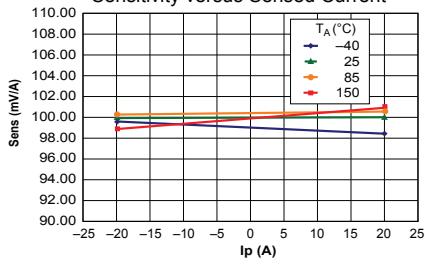
Nonlinearity versus Ambient Temperature



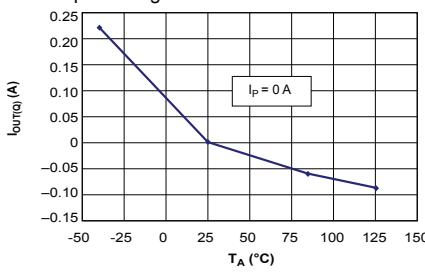
Sensitivity versus Ambient Temperature



Sensitivity versus Sensed Current



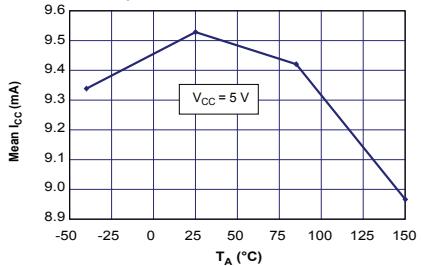
0 A Output Voltage Current versus Ambient Temperature



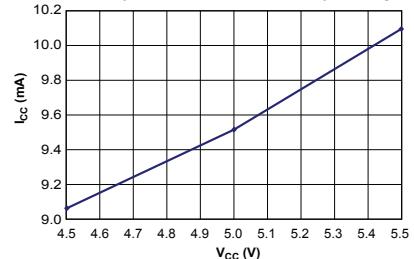
## CHARACTERISTIC PERFORMANCE

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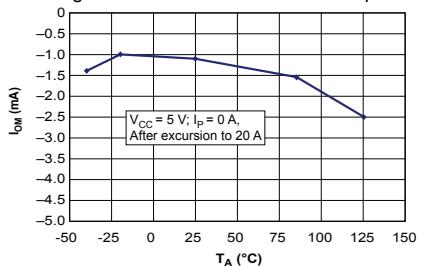
Mean Supply Current versus Ambient Temperature



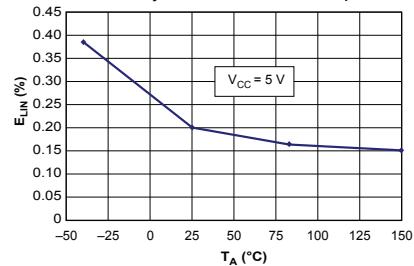
Supply Current versus Supply Voltage



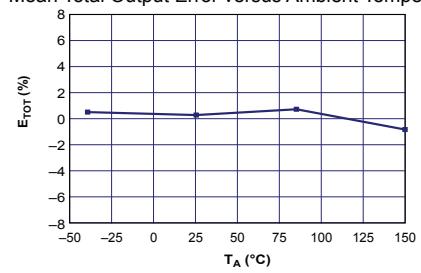
Magnetic Offset versus Ambient Temperature



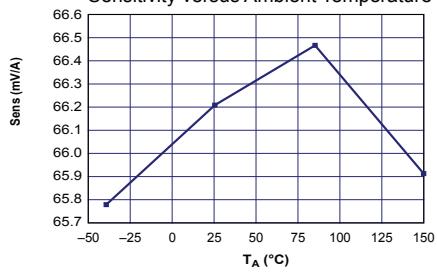
Nonlinearity versus Ambient Temperature



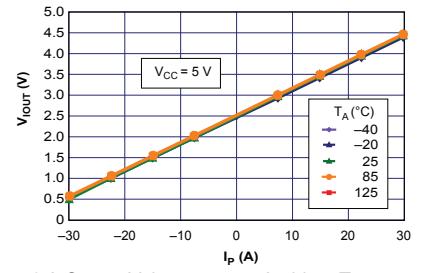
Mean Total Output Error versus Ambient Temperature



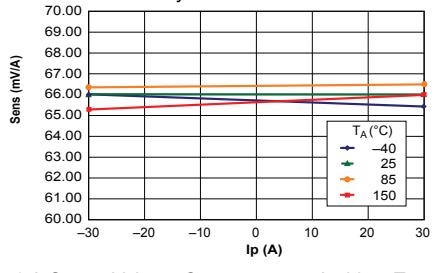
Sensitivity versus Ambient Temperature



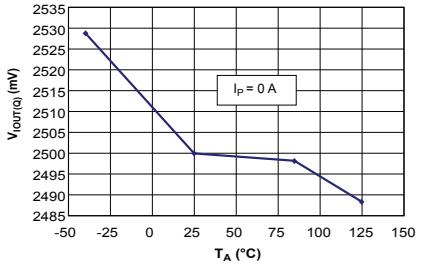
Output Voltage versus Sensed Current



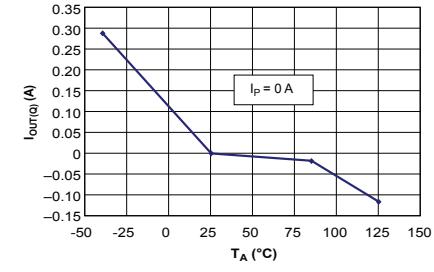
Sensitivity versus Sensed Current



0 A Output Voltage versus Ambient Temperature



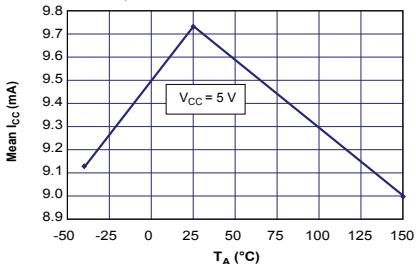
0 A Output Voltage Current versus Ambient Temperature



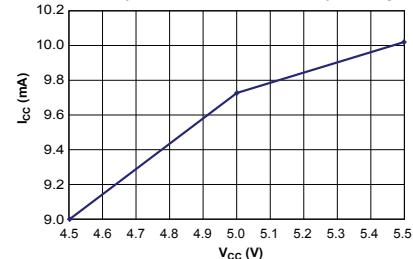
## CHARACTERISTIC PERFORMANCE

 $I_P = 50$  A, unless otherwise specified

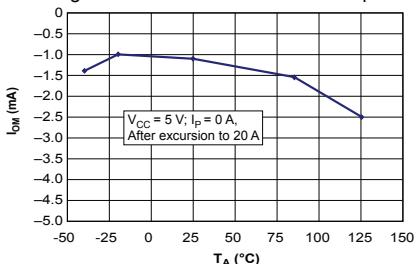
Mean Supply Current versus Ambient Temperature



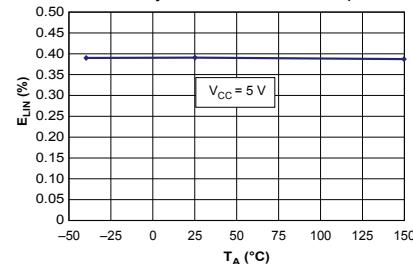
Supply Current versus Supply Voltage



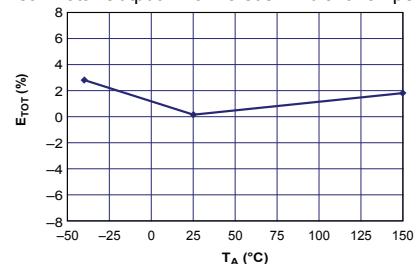
Magnetic Offset versus Ambient Temperature



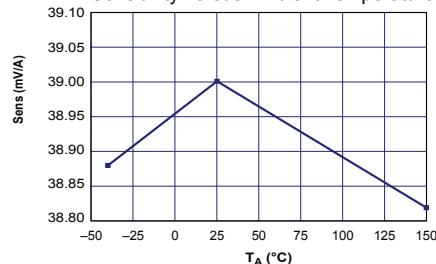
Nonlinearity versus Ambient Temperature



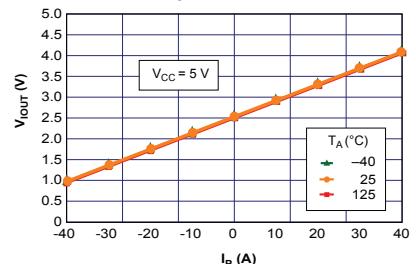
Mean Total Output Error versus Ambient Temperature



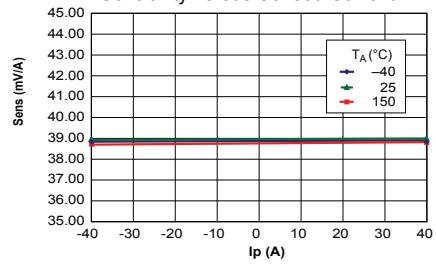
Sensitivity versus Ambient Temperature



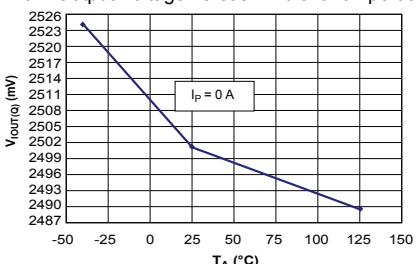
Output Voltage versus Sensed Current



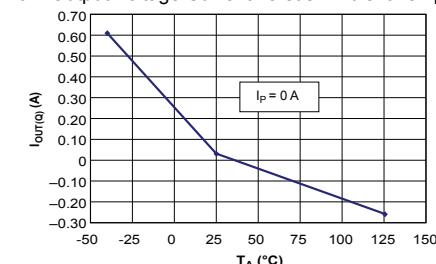
Sensitivity versus Sensed Current



0 A Output Voltage versus Ambient Temperature



0 A Output Voltage Current versus Ambient Temperature



## Definitions of Accuracy Characteristics

**Sensitivity (Sens).** The change in device output in response to a 1A change through the primary conductor. The sensitivity is the product of the magnetic circuit sensitivity (G/A) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

**Noise (V<sub>NOISE</sub>).** The product of the linear IC amplifier gain (mV/G) and the noise floor for the Allegro Hall effect linear IC ( $\approx 1$  G). The noise floor is derived from the thermal and shot noise observed in Hall elements. Dividing the noise (mV) by the sensitivity (mV/A) provides the smallest current that the device is able to resolve.

**Linearity (E<sub>LIN</sub>).** The degree to which the voltage output from the IC varies in direct proportion to the primary current through its full-scale amplitude. Nonlinearity in the output can be attributed to the saturation of the flux concentrator approaching the full-scale current. The following equation is used to derive the linearity:

$$100 \left\{ 1 - \left[ \frac{\Delta \text{ gain} \times \% \text{ sat} (V_{IOUT\_full-scale \text{ amperes}} - V_{IOUT(Q)})}{2 (V_{IOUT\_half-scale \text{ amperes}} - V_{IOUT(Q)})} \right] \right\}$$

where  $V_{IOUT\_full-scale \text{ amperes}}$  = the output voltage (V) when the sampled current approximates full-scale  $\pm I_p$ .

**Symmetry (E<sub>SYM</sub>).** The degree to which the absolute voltage output from the IC varies in proportion to either a positive or negative full-scale primary current. The following formula is used to derive symmetry:

$$100 \left( \frac{V_{IOUT\_+ \text{ full-scale \text{ amperes}} - V_{IOUT(Q)}}}{V_{IOUT(Q)} - V_{IOUT\_- \text{ full-scale \text{ amperes}}} \right)$$

**Quiescent output voltage (V<sub>IOUT(Q)</sub>).** The output of the device when the primary current is zero. For a unipolar supply voltage, it nominally remains at  $V_{CC}/2$ . Thus,  $V_{CC} = 5$  V translates into  $V_{IOUT(Q)} = 2.5$  V. Variation in  $V_{IOUT(Q)}$  can be attributed to the resolution of the Allegro linear IC quiescent voltage trim and thermal drift.

**Electrical offset voltage (V<sub>OE</sub>).** The deviation of the device output from its ideal quiescent value of  $V_{CC}/2$  due to nonmagnetic causes. To convert this voltage to amperes, divide by the device sensitivity, Sens.

**Accuracy (E<sub>TOT</sub>).** The accuracy represents the maximum deviation of the actual output from its ideal value. This is also known as the total output error. The accuracy is illustrated graphically in the output voltage versus current chart at right.

Accuracy is divided into four areas:

- **0 A at 25°C.** Accuracy at the zero current flow at 25°C, without the effects of temperature.
- **0 A over Δ temperature.** Accuracy at the zero current flow including temperature effects.
- **Full-scale current at 25°C.** Accuracy at the full-scale current at 25°C, without the effects of temperature.
- **Full-scale current over Δ temperature.** Accuracy at the full-scale current flow including temperature effects.

**Ratiometry.** The ratiometric feature means that its 0 A output,  $V_{IOUT(Q)}$ , (nominally equal to  $V_{CC}/2$ ) and sensitivity, Sens, are proportional to its supply voltage,  $V_{CC}$ . The following formula is used to derive the ratiometric change in 0 A output voltage,  $\Delta V_{IOUT(Q)RAT}$  (%).

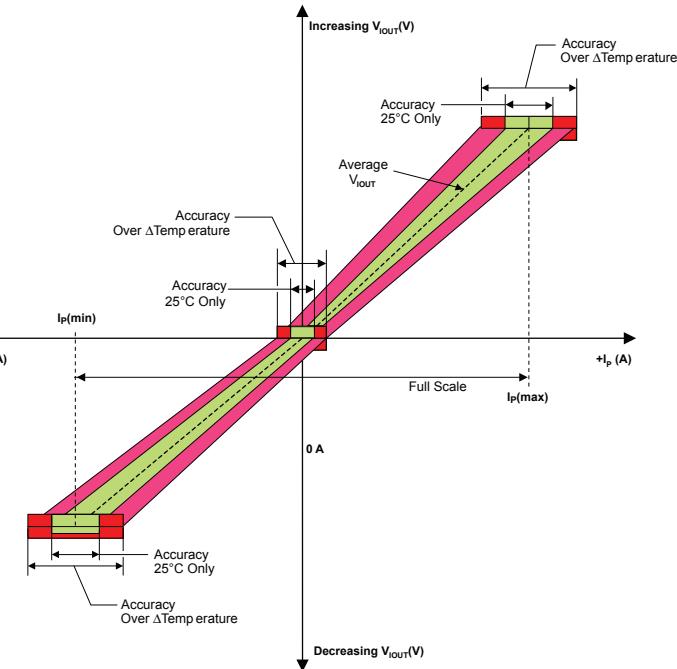
$$100 \left( \frac{V_{IOUT(Q)VCC} / V_{IOUT(Q)5V}}{V_{CC} / 5 \text{ V}} \right)$$

The ratiometric change in sensitivity,  $\Delta \text{Sens}_{RAT}$  (%), is defined as:

$$100 \left( \frac{\text{Sens}_{VCC} / \text{Sens}_{5V}}{V_{CC} / 5 \text{ V}} \right)$$

## Output Voltage versus Sampled Current

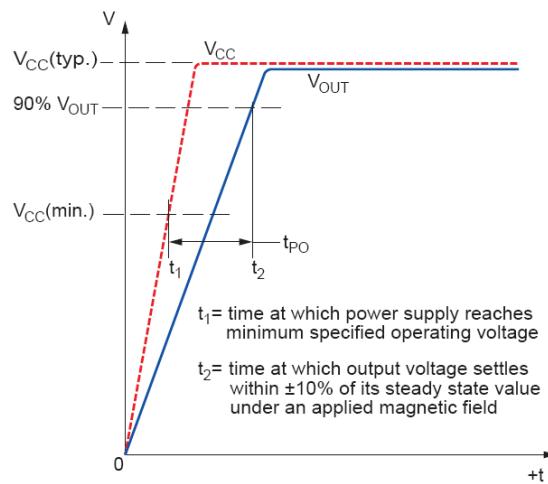
Accuracy at 0 A and at Full-Scale Current



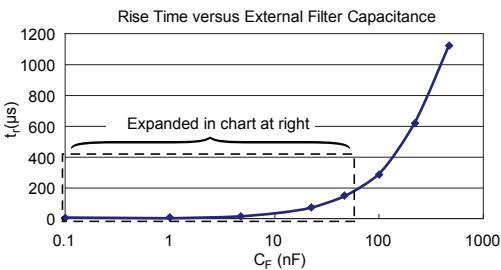
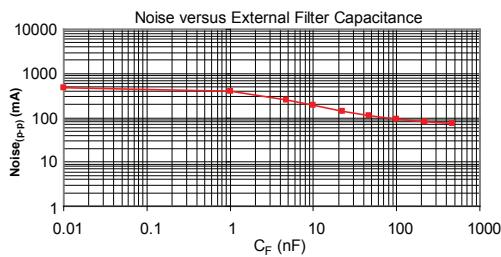
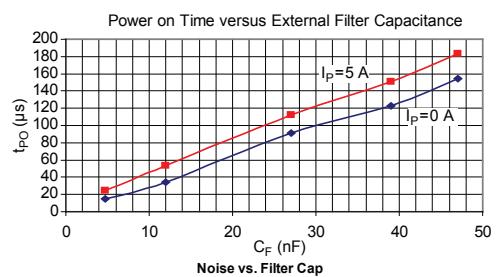
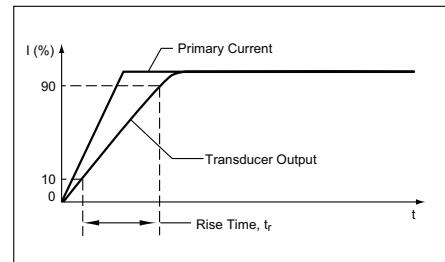
## Definitions of Dynamic Response Characteristics

**Power-On Time ( $t_{PO}$ ).** When the supply is ramped to its operating voltage, the device requires a finite time to power its internal components before responding to an input magnetic field.

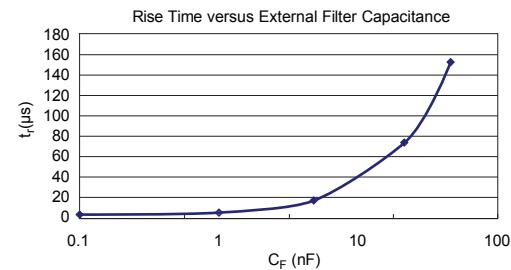
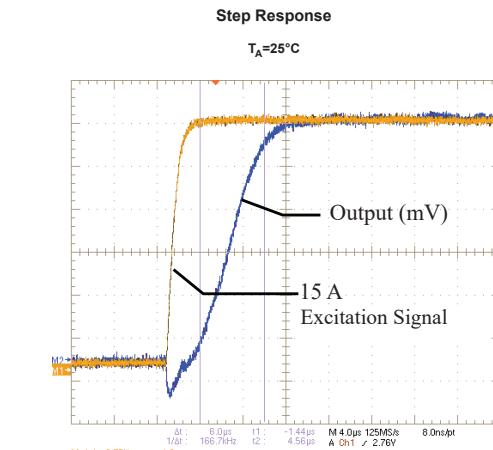
Power-On Time,  $t_{PO}$ , is defined as the time it takes for the output voltage to settle within  $\pm 10\%$  of its steady state value under an applied magnetic field, after the power supply has reached its minimum specified operating voltage,  $V_{CC}(\text{min.})$ , as shown in the chart at right.



**Rise time ( $t_r$ ).** The time interval between a) when the device reaches 10% of its full-scale value, and b) when it reaches 90% of its full scale value. The rise time to a step response is used to derive the bandwidth of the device, in which  $f(-3 \text{ dB}) = 0.35 / t_r$ . Both  $t_r$  and  $t_{\text{RESPONSE}}$  are detrimentally affected by eddy current losses observed in the conductive IC ground plane.



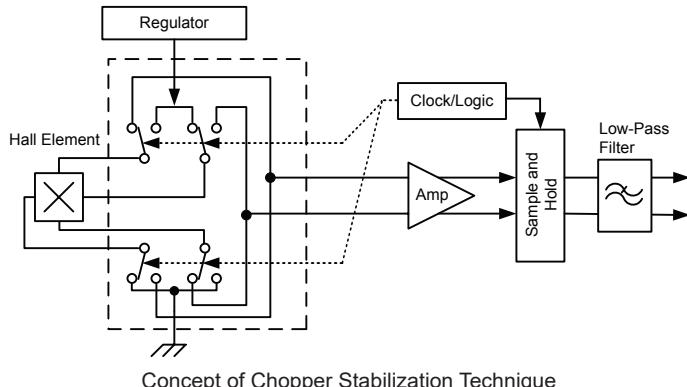
$C_F$ (nF)	$t_r$ (μs)
Open	3.5
1	5.8
4.7	17.5
22	73.5
47	88.2
100	291.3
220	623
470	1120



### Chopper Stabilization Technique

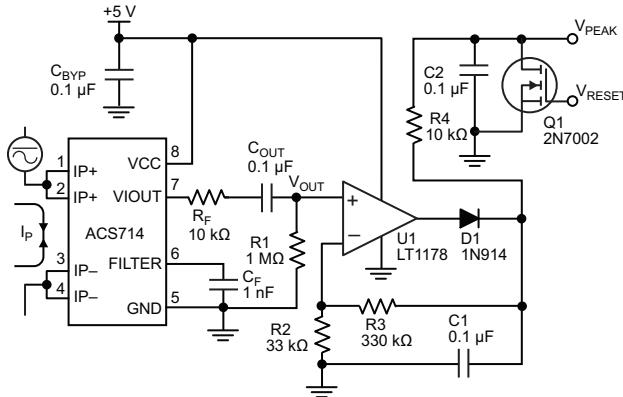
Chopper Stabilization is an innovative circuit technique that is used to minimize the offset voltage of a Hall element and an associated on-chip amplifier. Allegro has a Chopper Stabilization technique that nearly eliminates Hall IC output drift induced by temperature or package stress effects. This offset reduction technique is based on a signal modulation-demodulation process. Modulation is used to separate the undesired DC offset signal from the magnetically induced signal in the frequency domain. Then, using a low-pass filter, the modulated DC offset is suppressed while the magnetically induced signal passes through the filter. As a result of this chopper stabilization approach, the output voltage from the Hall IC is desensitized to the effects of temperature and mechanical stress. This technique produces devices that have an extremely stable Electrical Offset Voltage, are immune to thermal stress, and have precise recoverability after temperature cycling.

This technique is made possible through the use of a BiCMOS process that allows the use of low-offset and low-noise amplifiers in combination with high-density logic integration and sample and hold circuits.

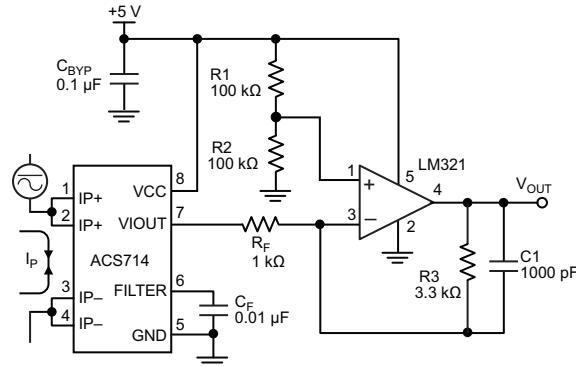


Concept of Chopper Stabilization Technique

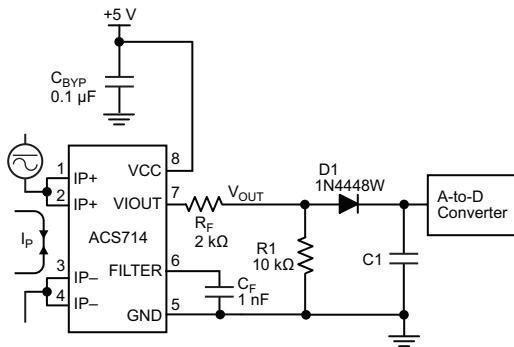
### Typical Applications



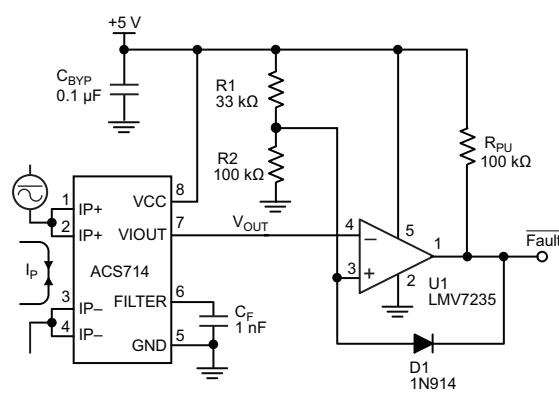
Application 2. Peak Detecting Circuit



Application 3. This configuration increases gain to 610 mV/A (tested using the ACS714ELC-05A).



Application 4. Rectified Output. 3.3 V scaling and rectification application for A-to-D converters. Replaces current transformer solutions with simpler ACS circuit. C1 is a function of the load resistance and filtering desired. R1 can be omitted if the full range is desired.



Application 5. 10 A Overcurrent Fault Latch. Fault threshold set by R1 and R2. This circuit latches an overcurrent fault and holds it until the 5 V rail is powered down.

## Improving Sensing System Accuracy Using the FILTER Pin

In low-frequency sensing applications, it is often advantageous to add a simple RC filter to the output of the device. Such a low-pass filter improves the signal-to-noise ratio, and therefore the resolution, of the device output signal. However, the addition of an RC filter to the output of a sensor IC can result in undesirable device output attenuation — even for DC signals.

Signal attenuation,  $\Delta V_{ATT}$ , is a result of the resistive divider effect between the resistance of the external filter,  $R_F$  (see Application 6), and the input impedance and resistance of the customer interface circuit,  $R_{INTFC}$ . The transfer function of this resistive divider is given by:

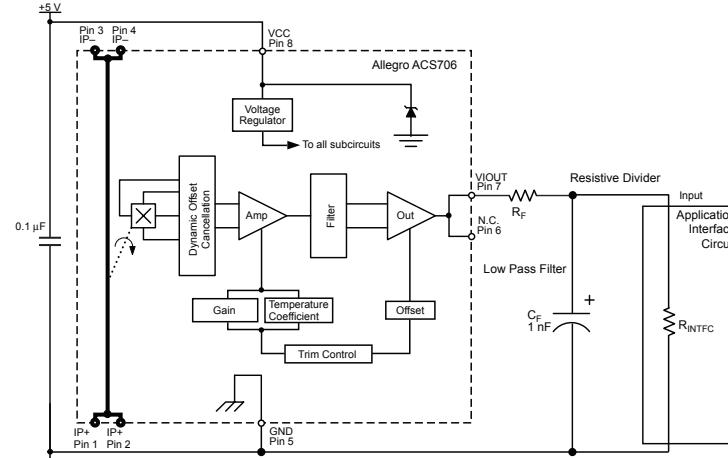
$$\Delta V_{ATT} = V_{IOUT} \left( \frac{R_{INTFC}}{R_F + R_{INTFC}} \right) .$$

Even if  $R_F$  and  $R_{INTFC}$  are designed to match, the two individual resistance values will most likely drift by different amounts over

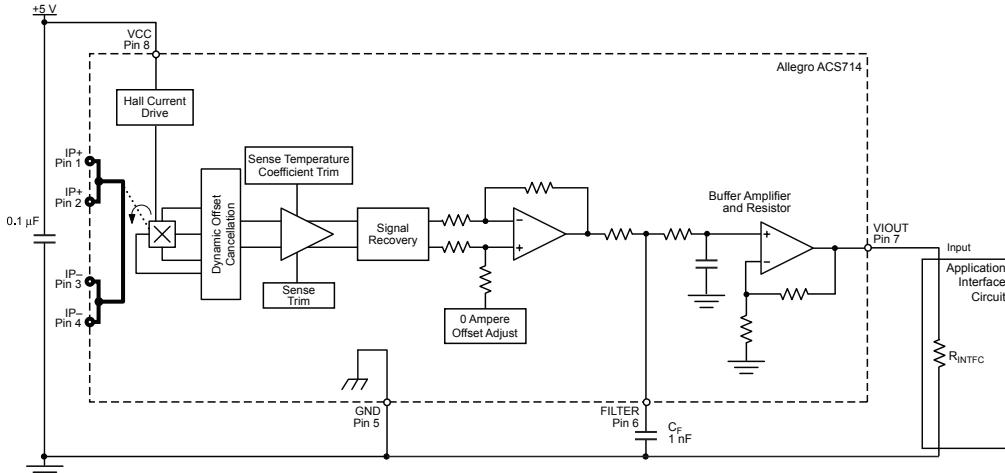
temperature. Therefore, signal attenuation will vary as a function of temperature. Note that, in many cases, the input impedance,  $R_{INTFC}$ , of a typical analog-to-digital converter (ADC) can be as low as 10 k $\Omega$ .

The ACS714 contains an internal resistor, a FILTER pin connection to the printed circuit board, and an internal buffer amplifier. With this circuit architecture, users can implement a simple RC filter via the addition of a capacitor,  $C_F$  (see Application 7) from the FILTER pin to ground. The buffer amplifier inside of the ACS714 (located after the internal resistor and FILTER pin connection) eliminates the attenuation caused by the resistive divider effect described in the equation for  $\Delta V_{ATT}$ . Therefore, the ACS714 device is ideal for use in high-accuracy applications that cannot afford the signal attenuation associated with the use of an external RC low-pass filter.

**Application 6.** When a low pass filter is constructed externally to a standard Hall effect device, a resistive divider may exist between the filter resistor,  $R_F$ , and the resistance of the customer interface circuit,  $R_{INTFC}$ . This resistive divider will cause excessive attenuation, as given by the transfer function for  $\Delta V_{ATT}$ :



**Application 7.** Using the FILTER pin provided on the ACS714 eliminates the attenuation effects of the resistor divider between  $R_F$  and  $R_{INTFC}$ , shown in Application 6.

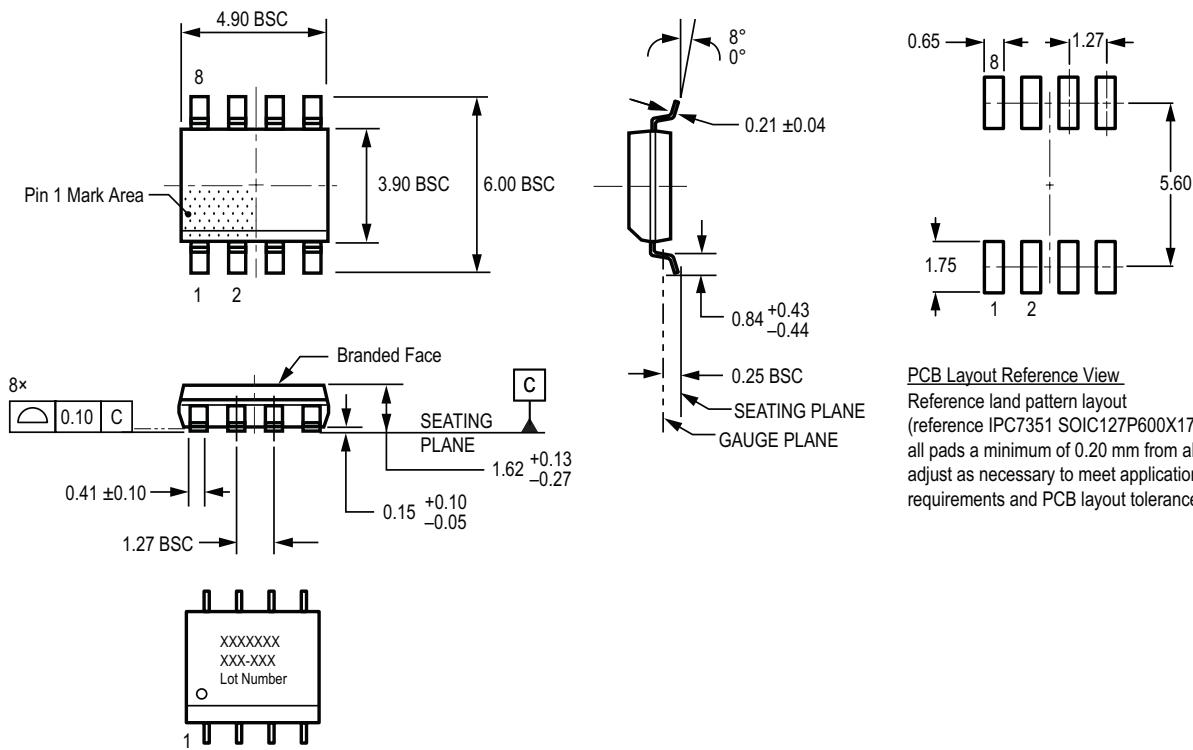


## Package LC, 8-pin SOIC

## For Reference Only; not for tooling use

(reference Allegro DWG-0000385, Rev. 2 or JEDEC MS-012AA)

Dimensions in millimeters – Not to scale

Dimensions exclusive of mold flash, gate burrs, and dambar protrusions  
Exact case and lead configuration at supplier discretion within limits shown

## PCB Layout Reference View

Reference land pattern layout  
(reference IPC7351 SOIC127P600X175-8M);  
all pads a minimum of 0.20 mm from all adjacent pads;  
adjust as necessary to meet application process  
requirements and PCB layout tolerances.

## Standard Branding Reference View

Lines 1, 2 = 8 characters

Line 3 = 5 characters

Line 1: Part Number

Line 2: Temp, Pkg - Amps

Line 3: First 5 Characters of Assembly Lot Number

Belly Brand: Country of Origin, Lot Number

Branding scale and appearance at supplier discretion

**Revision History**

Number	Date	Description
9	November 16, 2012	Update product selection, rise time, isolation, patents
10	June 5, 2017	Updated product status
11	December 10, 2018	Updated certificate numbers
12	May 17, 2019	Updated TUV certificate mark and minor editorial updates
13	February 3, 2020	Updated product status and minor editorial updates
14	February 7, 2022	Updated package drawing (page 17)
15	February 9, 2023	Updated selection guide heading (page 2)
16	March 12, 2025	Changed product status to not for new design (cover sheet), modified common thermal characteristics table (page 4), and removed patent number information (page 18)

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